

In the Specification:

Please replace the paragraph beginning on page 1, line 5 with the following amended paragraph:

The present invention relates to a method and apparatus for controlling a paging mode wherein paging data are RAKE-received by a ~~CDMA~~ Code Division Multiple Access (CDMA) wireless telephone terminal.

Please replace the paragraph beginning on page 2, line 13 with the following amended paragraph:

A next-generation portable wireless telephone system ~~IMT-2000~~ International Mobile Telecommunication -2000 (IMT-2000) employs the CDMA wherein transmission data are subjected to spread spectrum modulation by using a pseudo-noise code having a frequency bandwidth wider than that of the transmission data.

Please replace the paragraph beginning on page 2, line 18 with the following amended paragraph:

A signal transmitted from the base station is reflected and diffracted by mountains, buildings, and the like to reach a portable wireless terminal through a plurality of transmission paths. Therefore, the signal received by the portable wireless terminal consists of a plurality of waves. Therefore, as a receiving modulation system of the portable wireless terminal, a RAKE receiving system is used. In the RAKE receiving system, a received signal converted from a ~~RF~~ Radio Frequency (RF) bandwidth to a base band is despread, so that a plurality of waves included in the received wave are separated. The separated signals are combined to obtain a demodulation output of the received

Please replace the paragraph beginning on page 16, line 20 with the following amended paragraph:

A paging mode control unit 32 notifies a paging cycle measurement unit 4 of a paging cycle and instructs the paging cycle measurement unit 4 to start measurement of the paging cycle. The paging cycle is a cycle in which a base station intermittently outputs paging data. A paging mode control unit [[2]] 32 further instructs a system clock timing generation unit 3 to start or stop generation of a system clock. The paging mode control unit [[2]] 32 further notifies the RAKE receiving demodulator 31 of an operation start timing of delay profile forming and an operational resolution, and designates a synchronous capturing method. The operational resolution is a chip resolution or 1/4 chip resolution.

Please replace the paragraph beginning on page 17, line 26 with the following amended paragraph:

Fig. 5 shows an operation of the paging mode control apparatus according to this embodiment in a paging mode. The system clock timing generation unit 3 initially supplies a system clock to a RAKE receiving demodulation unit [[1]] 31. The paging mode control unit 32 notifies a paging cycle measurement unit 4 of a paging cycle before a paging mode is set (step S21). The paging cycle to be notified is set to be a paging cycle which is shorter than an actual paging cycle by several clocks in consideration of an offset of the count of paging clocks. The paging mode control unit 32 detects a paging timing (step S22).

Please replace the paragraph beginning on page 18, line 8 with the following amended paragraph:

When the paging timing is detected, the paging mode control unit 32 instructs the paging cycle measurement unit 4 to measure a paging cycle. The paging cycle measurement unit 4 starts to

count paging clocks. The paging mode control unit 32 further instructs the system clock timing generation unit 3 to stop the system clock (step S23). The system clock timing generation unit 3 stops the system clock by an instruction from the paging mode control unit [[2]] 32. The system clock is stopped to stop the function of the RAKE receiving demodulator [[1]] 31, so that the RAKE receiving demodulation unit [[1]] 31 is set a sleep mode.

Please replace the paragraph beginning on page 21, line 8 with the following amended paragraph:

The stop of the system clock shifts the RAKE receiving demodulator [[1]] 31 to a sleep mode. With respect to the paging cycle, a paging cycle counter value corresponds to $(n + 4)$, and the paging cycle measurement unit 4 notifies the paging mode control unit [[2]] 32 of an elapse of the paging cycle. At time T12 at which the paging counter value is "n", the system clock is started. When the system clock is started, the control operation is out of the sleep mode, and the paging mode control unit [[2]] 32 starts to form a delay profile at the chip resolution (time T13). The most effective path timing of the delay profile is detected, and a delay profile near the path timing is formed at the 1/4 chip resolution (time T14).